

<b>Notice of References Cited</b>	Application/Control No. 09/737,129	Applicant(s)/Patent Under Reexamination BAYLOR ET AL.	
	Examiner Syed J Ali	Art Unit 2127	Page 1 of 2

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,724,586	03-1998	Edler et al.	718/102
	B	US-5,745,778	04-1998	Alfieri, Robert A.	712/1
	C	US-5,974,438	10-1999	Neufeld, E. David	718/104
	D	US-6,289,369	09-2001	Sundaresan, Neelakantan	718/103
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Bellosa et al. "The Performance Implications of Locality Information Used in Shared-Memory Multiprocessors", Journal of Parallel and Distributed Computing, Vol. 37, No. 1, pp. 113-21, August 1996.
	V	Philbin et al. "Thread Scheduling for Cache Locality", ASPLOS VII, pp. 60-71, October 1996.
	W	Sinharoy, B. "Optimized Thread Creation for Processor Multithreading", The Computer Journal, 40(6), pp. 388-400, 1997.
	X	Nikolopoulos et al. "Efficient Runtime Thread Management for the Nano-Threads Programming Model", 12th International Parallel Processing Symposium and 9th Symposium on Parallel and Distributed Processing, pp. 183-94, March 1998.

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

77e

**Notice of References Cited**Application/Control No.  
09/737,129Applicant(s)/Patent Under  
Reexamination  
BAYLOR ET AL.Examiner  
Syed J AliArt Unit  
2127

Page 2 of 2

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Weissman, B. "Performance Counters and State Sharing Annotations: a Unified Approach to Thread Locality", ASPLOS VIII, pp. 127-38, October 1998.
	V	M. Steckermeier et al. "Using Locality Information in User Level Scheduling", University Erlangen-Nurnberg, Tech. Report TR-95-14, 1995.
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

Tce